

MOHAMED SATHAK A J COLLEGE OF ENGINEERING, CHENNAI - 603103

(An Autonomous Institution)

Tentative Timetable B.E./B.Tech. Degree Examinations - May-'26 / Jun-'26

Regulations - 2024

Choice Based Credit Systems (CBCS)

Branch Name: 204 B.Tech – Electronics Engineering w/s in VLSI Design and Technology

S.No	Sem	Sub.Code	Subject Name	Date	Session
1	1	24MA101	Matrices and Calculus	01-06-2026	FN
2	1	24CH111	Engineering Chemistry**	02-06-2026	AN
3	1	24EN101	Technical Communication - I	01-06-2026	AN
4	1	24TA101	Heritage of Tamils / தமிழ் மரபு	03-06-2026	AN
5	2	24MA202	Transforms and Fourier Analysis	04-06-2026	FN
6	2	24EN201	Technical Communication - II	05-06-2026	FN
7	2	24TA201	தமிழரும் தொழில்நுட்பமும் / Tamils and Technology	09-06-2026	FN
8	2	24EE201	Electric Circuit Analysis	05-06-2026	AN
9	2	24VL201	Theory of Semiconductor Devices	02-06-2026	FN
10	2	24GE101	Basic Civil and Mechanical Engineering	20-06-2026	FN
11	2	24CS111	Programming In C**	25-06-2026	FN
12	3	24EE311	Principles of Electrical Engineering**	08-06-2026	AN
13	3	24MA302	Linear Algebra and Numerical Method	06-06-2026	AN
14	3	24EC301	Signals and Systems	03-06-2026	FN
15	3	24EC312	Digital System Design**	06-06-2026	FN
16	3	24EC311	Analog Circuits - I**	04-06-2026	AN
17	4	24MA412	Statistics, Probability and Stochastic Process**	08-06-2026	FN
18	4	24EC412	Microcontroller and Interfacing**	10-06-2026	FN
19	4	24VL411	MOS Device Modelling and Simulation**	12-06-2026	FN
20	4	24AC411	Analog and Digital Communication**	15-06-2026	FN
21	4	24VL412	CMOS Digital VLSI Design	17-06-2026	FN
22	4	24EC411	Analog Circuits - II**	19-06-2026	FN



FN: FORENOON (10.00 A.M. TO 01. 00 P.M.)

AN: AFTERNOON (02.00 P.M. TO 5.00 P.M.)

DCoE

Controller of Examinations

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Mohamed Sathak AJ College of Engineering  
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Principal

Dr.K.S.SRINIVASAN, M.E., Ph.D  
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