

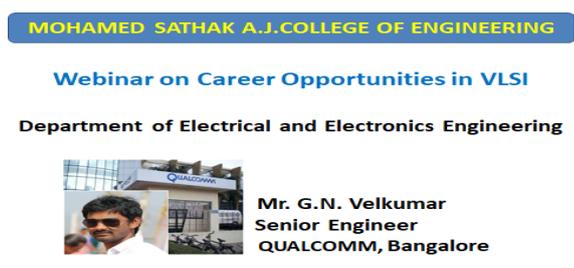
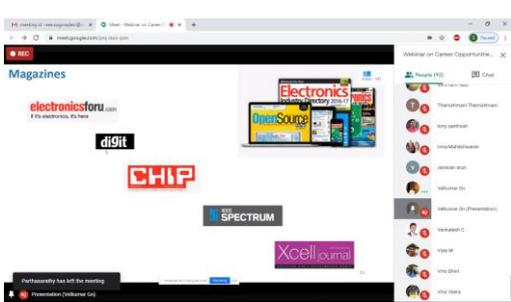
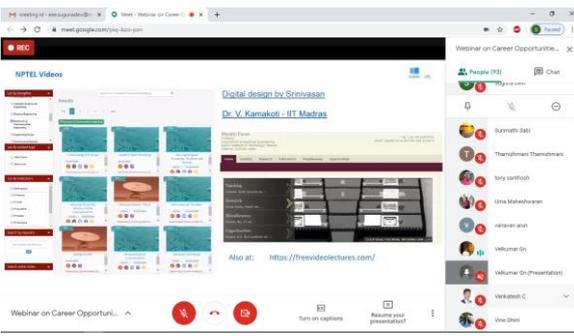
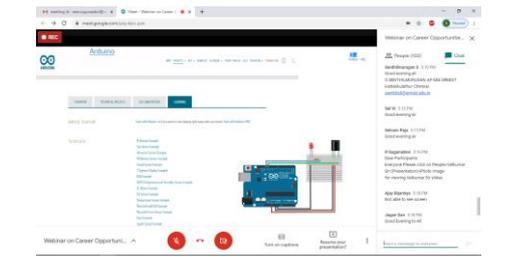
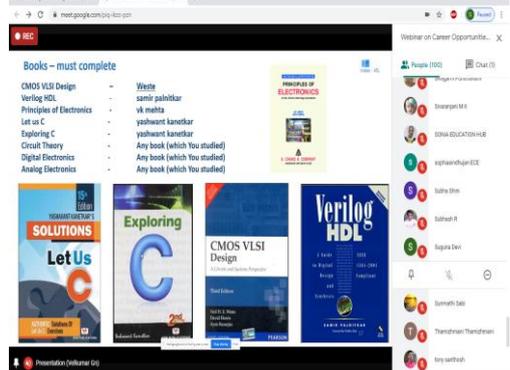
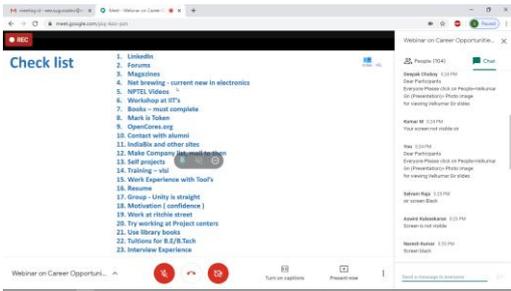


Department of Electrical and Electronics Engineering

REPORT ON EVENTS

Career Opportunities in VLSI

Department:	Electrical and Electronics Engineering	
Type of Event:	Webinar	
Event Title:	Career Opportunities in VLSI	
Date & Time:	06.06.2020 & 5:00PM –6:00 PM	
Venue / Mode (Platform):	Online Mode (Google Meet)	
Organizer:	Dr. I. Manju, HOD/EEE	
Coordinator:	Mrs.R.Suguna Devi , Assistant Professor/EEE	
Details of Participants:	External participants from various institution like Academicians, Researchers, Industry & Students and Faculty members of MSAJCE	
Profile of the Chief Guest(s)	Name of the Guest Speaker	Mr.G.N.Velkumar
	Organization	Qualcomm, Bangalore
	Designation	Senior Engineer
Write-up on Event	<p>The demand for the VLSI designers is quite high in order to develop FPGA implementations, System-on-chip and Application Specific Integrated Circuits. Semiconductor industry is growing fastest in India due to the Technology development in Intellectual Property from most of the companies from India provides services to MNCs. So we Department of EEE,MSAJCE taken the initiative to organise the webinar on Career Opportunities in VLSI for all the Students, Research Scholar and Faculty across India .</p> <p>The webinar started with 144 participants from different domain on 06.06.2020(Saturday) at 5.00 pm with welcome address and introduction of chief guest by Ms.S,Radhikha AP/EEE .</p> <p>The session was handled by the guest speaker Mr.G.N.Velkumar, Senior Engineer, Qualcomm, Bangalore .</p> <p>The speaker elaborated in detail about, what are the opportunities in the field of VLSI students and researchers, and also explains how to prepare resume with suitable examples to make the participants understand clearly. During the session he explained various formats and forms of resume used in different job positions. He also explained different domains like front end development and back end development in VLSI. In the discussion time with the participants, he answered all the questions patiently up to the participants' satisfaction.</p> <p>Ms.P.Nisha AP/EEE proposed the vote of thanks to the chief guest, Management, Participants and organizers.</p>	

		
<p>Photographs</p>		
		
	<p>VLSI Books to refer before applying for a Job</p>	<p>Check List before interview</p>
<p>Feedback from the participants</p>	<p>Mr.R Kishore Kumar , III Yr Student/EEE,KCT,Coimbatore: Session is very much interesting and gives direction to choose career</p> <p>Ms .Akila Seenu , AP/ECE,SJIT: Thank you for the informative presentation sir.</p> <p>Ms.V Vinodhini ,Teashing Fellow, University College Of Engineering, Ariyalur:Thanks. A great session.</p> <p>Ms.P.Dhanalakshmi : Good Presentation</p> <p>Mr. Adnan Hamid, II Yr/EEE:Good Session</p>	<p>Sanjana M,Student,SJIT: It is very useful and informative session for job seekers...conduct another webinar like this.</p> <p>Mr. M. Parthasarathy,PG Student: Excellent Session</p> <p>Ms. S. Nivetha ,II EEE/ Thanthai periyar government institute of technology, vellore: Thank you for wonderful session.</p> <p>Thank you sir, s nice and informative presentation sir</p> <p>Dr.K.R.Prabha,Associate Professor/SREC: Nice presentation sir</p>