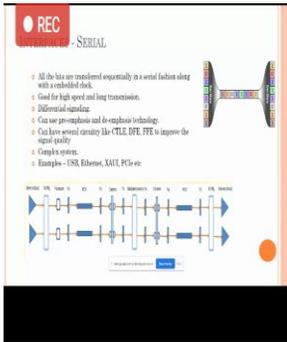
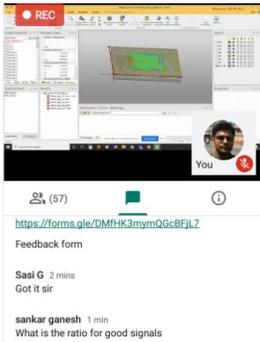


**REPORT ON EVENTS**

<b>Department:</b>	Electronics and Communication Engineering	
<b>Type of Event:</b>	WEBINAR	
<b>Event Title:</b>	SIGNAL AND POWER INTEGRITY IN SEMICONDUCTOR INDUSTRY	
<b>Date &amp; Time:</b>	27.06.2020 & 11.00 am - 12.00 pm	
<b>Venue / Mode(Platform):</b>	Online mode (Google meet)	
<b>Organizer:</b>	Dr.E.Dhiravidachelvi, HOD/ECE	
<b>Coordinator(s):</b>	1) S.Naveenkumar, AP/ECE 2) J.Raja, AP/ECE	
<b>Details of Participants:</b>	External participants from various institution like Academicians, Researchers, Industry & Students and faculty members of MSAJCE	
<b>Profile of the Chief Guest(s)</b>	Name of the Guest Speaker	Mr.Zakir Hussain M.E
	Organization	Microchip India Pvt Ltd, Bangalore, India
	Designation	Senior Analog Design Engineer
	Contact Number	99169 77390
	E-mail ID	
<b>Write-up on Event</b> (to publish in the college website)	<p>The webinar started with 65 participants from different domain on 27.06.2020 at 11.00 am with welcome address and introduction of chief guest by S.Naveenkumar (AP/ECE) (MSAJCE). The speaker elaborated in detail about, what is Signal and power Integrity in semiconductor Industry, and differentiated the same with suitable examples to make the participants understand clearly. During the session he explained various Tools and Types of Chips using in Core companies. During the discussion session, Speaker clarifying the doubts of the participants related to Signal and power Integrity in semiconductor Industry. J.Raja, AP/ECE proposed the vote of thanks to the chief guest, guest of honor, participants and organizers.</p>	
<b>Photographs</b>		
	Presentation by the speaker	Presentation by the speaker



Feed back and Questionary section

**Prepared By :**

**Reviewed / Forwarded by:**

**Signature:**

**Name:** Mr.S.Naveenkumar & Mr. J. Raja Dr.E.Dhiravidachelvi

**Designation:** Assistant professor Professor & Head

**Date:** 28.06.2020 28.06.2020

**Principal**