



MOHAMED SATHAK A.J. COLLEGE OF ENGINEERING
ISO 9001:2008 CERTIFIED INSTITUTION
(Approved by AICTE, NEW Delhi and affiliated to Anna University, Chennai)
34, Old Mahabalipuram Road SIPCOT – IT Highway
Chennai – 603 103.



FACULTY DATA SHEET

FACULTY NAME : KAYALVIZHIS
DESIGNATION : ASSISTANT PROFESSOR
DEPARTMENT : ECE
DATE OF BIRTH & AGE : 09-04-1984 &34
AADHAR NUMBER : 372203827395
PAN NUMBER : BDFPK7131P
PASSPORT NUMBER :
ADDRESS FOR COMMUNICATION : 6A, NORTH STREET,
MANAGIRI, MADURAI-20.
AREA OF INTEREST : VLSI DESIGN, NETWORKS
CONTACT NUMBER (OFFICE) : 9003788399 **Extn:**



EDUCATIONAL QUALIFICATION:

Category	Name of the Degree	Specialization	Year of Passing	Name of the University	% of Marks / Grades obtained	Class obtained
UG	B.TECH	INFORMATION TECHNOLOGY	2008	ANNA UNIVERSITY	75	SECOND CLASS
PG	M.E	VLSI DESIGN	2013	ANNA UNIVERSITY	87	FIRST CLASS WITH DISTINCTION
Ph.D.	Ph.D	Network-on-Chip	pursuing	ANNA UNIVERSITY		

ACADEMIC/INDUSTRIAL EXPERIENCE:

Name of the Organization	Designation	Nature of Work	Joining Date	Relieving Date	Experience		
					Years	Months	Days
RCET, Madurai	Lecturer	Teaching	Jan'2010	May 2012	02	04	-
C.R.Engg college, Madurai	Asst.Prof		June'2012	Dec'2012	00	06	-
Dhaanish Ahmed College of Engg, Chennai	Asst.Prof		Dec'2013	Sep'2018	04	09	-
Mohamed Sathak A.J. College of Engg, Chennai	Asst.Prof		Sep'2018	Till Date	00	05	-
Total					08	-	-

PUBLICATION DETAILS:

S.No	International/National Journal & Conference	Paper Title
1.	International Conference -2018	Efficient usage on mobile device through stylometry and Wi-Fi network
2	International Conference on Emerging Trends in Information and Communication -2017	Design of High performance adaptive routing with Optimized power in NoC
3	EMTIC-2015	Design of High performance adaptive routing in Network-on-Chip
4	8th National Conference on Recent Trends in Information and Communication Technologies (SPECTRUM'15)	Comparative Study on Buffered flow control in Network-on-Chip
5	International Conference on Nano Electronics and Nano Devices-2013	Throughput Improvement in NoC Based on Source Synchronous Backtracking wave pipeline switch
6	National conference on Recent Trends in Electrical and	Efficiency enhancement in Noc Based on Source Synchronous on-chip switch

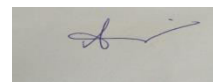
	Communication Engineering	
7	National Conference-IET'13	Data Transfer Rate Enhancement in NoC based on Source Synchronous on-chip switch
8	International journal for applied engineering research(IJAER)-2015	Design of High Performance Adaptive Routing in Network-on-Chip
9	Journal of social, technological and environmental science-2017	Design of High Performance Adaptive Routing With Optimized Power in Noc
10	National Conference on Innovations in Information and Communication Technology-2017	Effective Flood alert with Android and Emergency support in Network Unidentified Zone
11	Internal Journal of Advance Research in Science and Engineering	Efficient usage on mobile device through stylometry and Wi-Fi network

RESEARCH GRANT / CONSULTANCY (Sanctioned/Ongoing)

S.No	Agency	Grant/Consultancy Details
1	NIT Manipur	<p>E-Content Development and preparing video class for NIT Manipur on the subjects</p> <ul style="list-style-type: none"> Analog and Digital CMOS IC design (ME VLSI systems & Embedded systems) Antenna Design (BE- ECE)

Any Other information:

It is certified that all the information provided are true to the best of my knowledge.



Signature of the Faculty